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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use several sheets if necessary)

APPLICANT  
H. TANAKAFILING DATE  
January 28, 2005GROUP  
2111

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
/KK/	5,752,035	5/1998	TRIMBERGER	717	153	

**FOREIGN PATENT DOCUMENTS**

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT	
						YES	NO
/KK/	EP 1 059 580 A1	12/2000	EUROPE	-	-	X	

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

/KK/	Tubella, et al., "Control speculation in multithreaded processors through dynamic loop detection", High Performance Computer Architecture 1998. Proceedings., 1998 Fourth International Symposium on Las Vegas, NV, USA, 1-4 Feb. 1998, Los Alamitos, CA, USA, IEEE Comput. Soc, US, 1 February 1998, pages 14-23, XP 010266834.
/KK/	Furber, "ARM System-on-Chip architecture", 2000, Addison-Wesley, London, XP002415715, pages 136-141.

EXAMINER /Kenneth Kim/

DATE CONSIDERED

04/20/2007

EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.